

S/N 09/852,839

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	KONDO, et al.	Examiner:	V. Nguyen
Serial No.:	09/852,839	Group Art Unit:	2858
Filed:	May 10, 2001	Docket No.:	10873.713US01
Title:	SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME, AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT HAVING THE SAME		

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, Mail Stop Amendment, P.O. Box 1450, Alexandria, VA 22313-1450 on September 28, 2004.

By: Peggy Kerkhove
Name: Peggy Kerkhove

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(b))

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner. At least some of the references were recently cited in a Japanese Office Action mailed September 15, 2004.

This statement should be considered because it is submitted before the mailing of a first Office Action after the filing of a Request for Continued Examination under 37 C.F.R. § 1.114 or a CPA under 37 C.F.R. § 1.53(d). Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

In accordance with 37 C.F.R. § 1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided. Enclosed for the Examiner's information is a copy of the Japanese Office Action.

A concise explanation of the relevance of each non-English language document or other information is as follows (37 C.F.R. § 1.98(a)(3)):

US 6,229,379 is an English equivalent of JP 11-297950 and US 6,195,305 is an English equivalent of JP 07-219658. An English abstract has been included for JP 11-150230.

No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

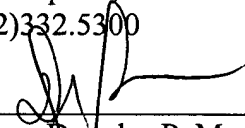
Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

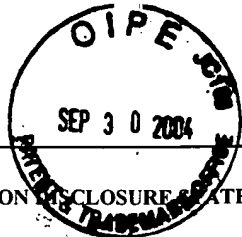
MERCHANT & GOULD P.C.
P.O. Box 2903
Minneapolis, MN 55402-0903
(612)332.5300

Dated: September 28, 2004

By: 
Douglas P. Mueller
Reg. No. 30,300

DPM/pjk





FORM 1449*

INFORMATION DISCLOSURE STATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number:

10873.713US01

Application Number:

09/852,839

Applicant: KONDO, et al.

Filing Date: May 10, 2001

Group Art Unit: 2858

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,195,305	February, 2001	Fujisawa, et al.			
	6,229,379	May, 2001	Okamoto			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	7-219658	August, 1995	Japan			Abstract	
	11-150230	June 1999	Japan				
	11-297950	October, 1999	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

23552

PATENT TRADEMARK OFFICE

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KONDO, et al.

Examiner: V. Nguyen

Serial No.: 09/852,839

Group Art Unit: 2858

Filed: May 10, 2001

Docket: 10873.713US01

Confirmation No.: 5451

Notice of Allow. n/a

No.:

Date:

Due Date: n/a

Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME, AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT HAVING THE SAME



2858
JFW/93

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By:

Name: Peggy Kerkhove

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

23552

PATENT TRADEMARK OFFICE

Sir:

We are transmitting herewith the attached:

- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Supplemental Information Disclosure Statement, Form 1449, 5 Reference(s), Copy of Japanese Office Action
- ☒ Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C.
P.O. Box 2903, Minneapolis, MN 55402-0903
612.332.5300

By:

Name: Douglas P. Mueller

Reg. No.: 30,300

DPM/pjk